

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claim in the application:

Listing of Claims:

Claim 1: (Currently amended) A surge suppressor to suppress transient energy to a load from an ac power supply, comprising:

(a) a diode bridge electrically coupled to the ac power supply for supplying electrical power to the load;

(b) a plurality of capacitor units in a bank electrically coupled to said diode bridge to clamp the transient energy, at least one of said capacitor units having a capacitor combined in parallel with a resistor to discharge the transient energy until a non-transient operating voltage is obtained; and

(c) a control unit to safely energize the suppressor and monitor operational status of the suppressor.

Claim 2: (Previously presented) The surge suppressor of claim 1 wherein said diode bridge comprises a capacitor and a resistor connected in shunt with a switching device to limit a rate of voltage increase across said load when switching from a conducting state to a blocking state.

Claim 3: (Previously presented) The surge suppressor of claim 2 wherein said capacitor and said resistor limit a peak voltage across said load when said load in subjected to the transient energy.

Claim 4: (Cancelled)

Claim 5: (Currently amended) The surge suppressor of claim [[4]] 1 wherein each said combination capacitor unit and parallel resistor contain an overcurrent protective device.

Claim 6: (Previously presented) The surge suppressor of claim 5 wherein said overcurrent protective device is a normally-closed, dual element time delay fuse.

Claim 7: (Previously presented) The surge suppressor of claim 6 wherein said control unit comprises a capacitor failure relay controlled by a plurality of said dual element time delay fuse wherein if at least one of said plurality of dual element time delay fuses fails said capacitor failure relay deenergizes and a fuse failure indicating device is energized.

Claim 8: (Previously presented) The surge suppressor of claim 1 wherein said control unit comprises a plurality of phase loss relays electrically connected to a three-phase ac power supply to indicate that a supply voltage is within a normal tolerance.

Claim 9: (Previously presented) The surge suppressor of claim 8 wherein said plurality of phase loss relays are connected in series with an auxiliary control relay such that a phase loss condition will cause said auxiliary control relay to open.

Claim 10: (Previously presented) The surge suppressor of claim 9 wherein said auxiliary control relay has a contact connected to a timing relay which ensures that said plurality of capacitor units in a bank are precharged prior to closing a bypass contactor.

Claim 11: (Previously presented) The surge suppressor of claim 9 wherein said control unit comprises a reset element in series with said auxiliary control relay to reset the surge suppressor in the event of a phase loss condition.

Claim 12: (Previously presented) The surge suppressor of claim 11 wherein said reset element is a selector switch.

Claim 13: (Currently amended) A surge suppressor to suppress transient energy to a load from an ac power supply, comprising:

(a) a diode bridge electrically coupled to the ac power supply for supplying electrical power to the load;

(b) a plurality of capacitor units in a bank electrically coupled to said diode bridge to clamp the transient energy;

(c) a control unit to safely energize the suppressor and monitor operational status of the suppressor; and

(d) a precharge unit electrically coupled to said plurality of capacitor units for limiting an inrush current into said plurality of capacitor units upon initialization of the surge suppressor; and

(e) an overcurrent protective device for the bank of capacitor units, the overcurrent protective device comprising a normally-closed, dual element time delay fuse.

Claim 14: (Previously presented) The surge suppressor of claim 13 wherein said diode bridge comprises a capacitor and a resistor connected in shunt with a switching device to limit a rate of voltage increase across said load when switching from a conducting state to a blocking state.

Claim 15: (Previously presented) The surge suppressor of claim 14 wherein said capacitor and said resistor limit a peak voltage across said load when said load is subjected to the transient energy.

Claim 16: (Previously presented) The surge suppressor of claim 13 wherein each said capacitor unit is combined with a parallel resistor to discharge the transient energy until a non-transient operating voltage is obtained.

Claim 17: (Cancelled)